



# BARASAT GOVERNMENT COLLEGE

## TEACHER'S PROFILE

*DR MADHUSUDAN GHOSH, DEPARTMENT OF PHYSICS*

- **DESIGNATION** : Associate Professor
- **QUALIFICATION** : M.Sc., Ph.D.
- **DATE OF JOINING THE SERVICE** : Jan 4, 2002
- **DATE OF JOINING THE INSTITUTION** : Sep 1, 2020
- **ADDRESS FOR COMMUNICATION** : Dept of Physics, Barasat Govt. College, 10, K.N.C Road, Barasat, North 24 Parganas, West Bengal, PIN-700124
- **PHONE NO** : Private information
- **EMAIL ADDRESS** : madhudusdan.ghosh@bgc.ac.in
- **SPECIALIZATION** : Radiophysics & Electronics
- **TEACHING EXPERIENCE** : UG course from Jan,2002 till date and PG course from Sept,2020 till to date
- **COLLEGE SERVED** : (1) A.B.N. Seal College, Coochbehar (from Jan,2002 to July,2006)  
(2) Krishnanagor Govt College (from July,2006 to July,2009)  
(3) Maulana Azad College (from July,2009 to June,2019)  
(4) Singur Govt. College (from July,2019 to Aug,2020)  
(5) Barasat Govt. College (from Sept,2020 till date)

<b>➤ ACADEMIC AND ADMINISTRATIVE EXPERIENCE</b>	: Reviewer of International Journal of Electronics, UK, Tabulation work (from 2002 till date), Area & Pay-fixation committee (from 2009 to 2016), Income Tax committee (from 2007 to 2016), Admission Committee (from 2003 to 2019), NAAC committee (from 2012 to till date), IQAC (from 2019 till date), Exam committee (more than 10 Years), Disciplinary committee, HOD (from July,2019 to Aug,2020) at Singur Govt college Using of different softwares for online laboratory experiment, Developed different Excel programs for academic & administrative interest
<b>➤ TOPICS TAUGHT</b>	: Electronics (Digital & Analog), Communication Principles, Electricity & Magnetism, Relativity
<b>➤ AREA OF RESEARCH &amp; INTEREST</b>	: Phase-Locked-Loop (PLL), Nonlinear dynamics, Communication systems, data clock recovery circuit
<b>➤ ONGOING PROJECT DETAILS</b>	: NONE
<b>➤ AWARD RECEIVED</b>	: NONE
<b>➤ PATENT DETAILS</b>	: NONE
<b>➤ EXTRACURRICULAR ACTIVITIES</b>	: NONE
<b>➤ CAREER PROFILE</b>	: I received B.Sc.(Physics), M.Sc.(Physics) and Ph.D.(Science) degree from Burdwan University, West Bengal, India in the year 1997, 1999 and 2013 respectively. At present I holds a faculty positions at the department of Physics, Barasat Govt. College, Barasat, North 24 PGS, West Bengal, India. My research interest centres on field of synchronous communication system and nonlinear dynamics of discrete controlled systems. I have published about 9 research papers in inland and foreign journals and has contributed about 4 technical papers in national and international seminars and conferences. Completed UGC-Minor Research project from March,2014 to March,2016 (Sanctioned amount Rs.4.5 Lakh)
<b>➤ ACADEMIC LINK</b>	: <a href="https://scholar.google.com/citations?user=6QeLZxEAAAAJ&amp;hl=en">https://scholar.google.com/citations?user=6QeLZxEAAAAJ&amp;hl=en</a>

# PUBLICATION

## JOURNAL PUBLICATION :

- (1) M. Ghosh, 'Nonlinear Dynamics and Chaotic Behavior of Delayed Digital Phase Locked Loop', Scientific Voyage, Oct, 2015, ISSN No: 2395-5546
- (2) M. Ghosh, 'Additive Noise Response of Some Novel Phase Detector Based Charge Pump PLL Circuits; an Analytical and Simulation', Indian Journal of Applied Research (IJAR), Oct, 2015, ISSN No: 2249-555X
- (3) M. Ghosh, 'Improvement of the Performance of DPLL Based FM Demodulator by using of Variable Gain Control in the Positive Region of the Input Signal', International Journal of Advanced Research in Electronics and Communication Engineering (IJARECE), May, 2015, ISSN: 2278 – 909X
- (4) M Ghosh, S. Dutta, 'A Fast Acquisition Data Clock Recovery Circuit', MAC Journal of Basic and Applied Sciences, March, 2015, ISSN: 2347-5366
- (5) M Ghosh, 'Performance Enhancement of Digital Phase Locked Loop (DPLL) Based FM Demodulator by Using of Variable Gain Control in the Negative Region of the Input Signal', MAC Journal of Basic and Applied Sciences, March, 2015, ISSN: 2347-5366
- (6) M. Ghosh, T. Banerjee and B. C. Sarkar, 'Nonlinear Dynamics and Chaos in Second Order ZC1-DPLLs with Inherent Time Delay', International Journal of Engineering and Advanced Technology (IJEAT), Aug, 2012, ISSN: 2249 – 8958
- (7) M. Ghosh, T. Banerjee and B. C. Sarkar, 'Design limitations and its effect in the performance of ZC1-DPLL', ACEEE International Journal on Communication, March,2012, ISSN: 2158-7558
- (8) M. Ghosh, Dr A. Hati, Dr B. C. Sarkar, 'On improving the spectral purity of the regenerated clock signal in a data clock recovery circuit', Indian Journal of Engineering and Material Science (IJEMS), August, 2002, ISSN: 0971-4588 (Print)
- (9) M. Ghosh, Dr A. Hati, Dr B. C. Sarkar, 'phase detector for data clock recover circuit', Institute of Electrical Engineers (IEE, Electronics Letters), UK, February, 2002, ISSN: 0099-2887



**BOOK PUBLICATION**

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